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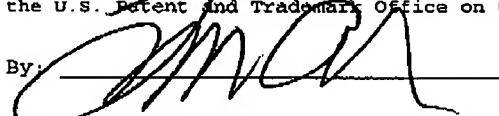
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1999P1778

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By: 

Date: August 23, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/002,925 Confirmation No. 5108
Applicant : Andreas Kux et al.
Filed : November 2, 2001
TC/A.U. : 2813
Examiner : Nema O. Berezny
Title: : Method and Device for Securing a Multi-Dimensionally Constructed Chip Stack and Chip Configuration

Docket No. : 1999P1778

Customer No. : 24131

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT under 37 C.F.R. § 1.312

Sir:

In response to the Notice of Allowability dated June 29, 2004,
please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of
claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 11 of this paper.